



PATENT
51270-277019

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:)

NOBUAKI TSUJI, et al.)

Serial No: 09/982,335)

Filed: October 18, 2001)

For: SEMICONDUCTOR INPUT
PROTECTION CIRCUIT)

Art Unit: 2811

Examiner: Ori Nadav

PETITION FOR EXTENSION OF TIME

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450, on <u>January 8, 2004</u> Date of Deposit <u>Mark R. Kendrick</u> Name <u><i>Mark R. Kendrick</i></u> 1/8/04 Signature Date	
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In accordance with Rule 136, Applicant respectfully petitions the Commissioner for a 3-month extension of time extending to January 8, 2004, the period for response to the Office Action dated July 8, 2003. A check for \$950 and the responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 16-1805. A copy of this petition is enclosed.

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Respectfully submitted,

PILLSBURY WINTHROP LLP

Date: January 8, 2004

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